Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

APPLICANT James E. O'Toole et al.

FILING DATE Filed Herewith

Filed Herewith U.S. PATENT DOCUMENTS Subclass *Examiner Document Date Name Class Filing Date If Appropriate Initial Number ESS 01/67 AA3,299,424 Vinding 12/74 Works et al. AB 3,852,755 4.075,632 02/78 Baldwin et al. AC 4,572,976 02/86 AD Fockens 4,656,463 04/87 Anders et al. ΑE AF 4,700,179 10/87 Fancher AG 4,724,427 02/88 Carroll ΑH 4,743,864 05/88 Nakagawa et al. EA 4,800,543 01/89 Lyndon-James et al. ΑI ΑJ ΑK FOREIGN PATENT DOCUMENTS Date Country Class Subclass Translation Number Yes No AM ΑN ΑO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR "Micron Morning Report", The Idaho Statesman, July 16, 1993. 521 "A Low-Power Spread Spectrum CMOS RFIC for Radio Identification Applications", by John R. Tuttle, Conference Proceedings from RF Expo West, pp. 216-222, March 22-24, 1994, San Jose, CA. ΑT Provisional Application, Serial No. 60/023,321, Filed July 30, 1996. DATE CONSIDERED 3-19-04 **EXAMINER**

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AD	4,85	4,328	08/89	Pollack	· - ·)		
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	AF	4,86	2,160	08/89	Ekchian et al.		_		_		
	AG	4,87	0,419	09/89	Baldwin et al.		-				
	AH	4,88	8,591	12/89	Landt et al.						
	AI	4,89	0,072	12/89	Espe et al.						
	AJ	4,91	2,471	03/90	Tyburski et al.		-	-		-	
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	AR		"CMOS Analog In	tegrated Circu	its Based on Weak Inversion Oper	ation", by Eric Vittoz	and Jean F	ellrath,	IEEE	Journal of	
Set					12, No. 3, June 1977						
	AS		Mitsubishi Motors						-	-	
TAR	-										
	AT		"Digital RF/ID En	hances GPS",	by John R. Tuttle, Proceedings of	the Second Annual V	Vireless Sym	posium,	рр.400	6-411,	
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Sheet 3 of 9 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. Form PTO-1449 PATENT AND TRADEMARK OFFICE MI40-325)9/822463 LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) James E. O'Toole et al. GROUP FILING DATE 2635 Filed Herewith U.S. PATENT DOCUMENTS Date Class Subclass Filing Date *Examiner Document Name Number If Appropriate Initial 5,030,807 07/91 Landt et al. AA 5,075,691 12/91 ΑB Garay et al. 5,081,458 01/92 AC Meunier Hassett et al. AD 5,086,389 02/92 AE 5,122,687 06/92 Schmidt ΑF 5,128,938 07/92 AG 5,130,668 07/92 Emslie et al. ΑH 5,134,085 07/92 Gilgen et al. ΑI 5,142,292 08/92 Chang ΑJ 5,144,314 09/92 Malmberg et al. ΑK 09/92 5,151,624 Stegherr et al. FOREIGN PATENT DOCUMENTS Class Subclass Date Translation Document Country Number No AL AM AN ΑO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR Provisional Application, Serial No. 60/023,318, Filed July 30, 1996. 58A "Micron RFID Communications Protocol Manual," July 22, 1993, Pre-Release Version 0.95, pp. 1-71. AS ΑT Multer & Kamins, DEVICE ELECTRONICS FOR INTEGRATED CIRCUITS, 1977, John Wiley & Sons, 2nd Edition, Pages 157-160.

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SA	AA	5,15	3,583	10/92	Murdoch	•		<u> </u>					
1	AB	5,16	4,985	11/92	Nysen et al.				-,				
	AC	5,17	5,774	12/92	Truax et al.		(
	AD	5,20	6,609	04/93	Mijuskovic								
	AE	5,21	8,343	06/93	Stobbe et al.		-						
	AF	5,23	1,273	07/93	Caswel		_						
	AG	5,25	2,979	10/93	Nysen				,				
	AH	5,27	2,367	12/93	Dennison et al.				-				
	AI	5,28	7,112	02/94	Schuermann		7						
1	AJ	5,29	4,928	03/94	Cooper et al.				•				
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	AT		Paul R. Grav et al	., "Analysis a	nd Design of Analog Integrated Ci	rcuits," John Wilev &	Sons, Inc.	1977. n	ър. 667-	-681.			
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ATTY. DOCKET NO. MI40-325 U.S. DEPARTMENT OF COMMERCE SERIAL NO. 第2 Form PTO-1449 PATENT AND TRADEMARK OFFICE LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) James E. O'Toole et al. GROUP FILING DATE Filed Herewith U.S. PATENT DOCUMENTS Class Subclass Document Date Name Filing Date If Appropriate Initial Number 5,311,186 05/94 Utsu et al. AA 5,323,150 06/94 Tuttle ΑB 5,355,513 10/94 Clarke et al. AC 11/94 Snodgrass et al. AD 5,365,551 5,374,930 12/94 AE Schuermann 02/95 Silvey et al. AF 5,394,444 AG 5,406,263 04/95 Tuttle 5,412,351 05/95 ΑH Nystrom et al. ΑI 5,420,757 05/95 Eberhardt et al. ΑJ 5,430,441 07/95 Bickley et al. 5,444,223 08/95 Blama FOREIGN PATENT DOCUMENTS Document Date Country Class Subclass Translation Number Yes No ΑM ΑN ΑO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) Donald O. Pederson, "Analog Integrated Circuits for Communication, Principles, Simulation and Design," Kluwer Academic Publishers, 1991, pp. 431-433. Barrie Gilbert, "A Precise Four-Quadrant Multiplier with Subnanosecond Response," IEEE Journal of Solid-State Circuits, 1968, AS pp. 365-373. ΑT DATE CONSIDERED 13-19-04 **EXAMINER** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AB	5,448,110	09/95	Tuttle et al.					
	AC	5,448,242	09/95	Sharpe et al.		/			
	AD 5,448,772		09/95	Grandfield			_		
	AE	5,450,087	09/95	Hurta et al:		_			
	AF	5,461,385	10/95	Armstrong					
	AG	5,471,212	11/95	Sharpe et al.		_			
	АН	5,478,991	12/95	Watanabe et al.					
	AI	5,489,546	02/96	Ahmad et al.		_	f		
	LA	5,499,214	03/96	Mori et al.			_		
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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. PATENT AND TRADEMARK OFFICE M140-325 LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) James E. O'Toole et al. GROUP Z FILING DATE Filed Herewith U.S. PATENT DOCUMENTS Date Class Subclass Filing Date *Examiner Document Name Number lf Appropriate Initial $\mathbf{A}\mathbf{A}$ 5,686,920 11/97 Hurta et al. ΑB 5,703,509 12/97 Hirata AC 5,719,550 02/98 Bloch et al. 5,721,678 AD 02/98 Widl ΑE 5,721,783 02/98 Anderson ΑF 5,726,630 03/98 Marsh et al. AG 5,815,042 09/98 Chow et al. ΑH 5,901,349 05/99 Guegnaud et al. Αl 4,746,830 05/88 Holland AJ 4,786,903 11/88 Grindahl et al. ΑK 4,952,889 08/90 Irwin et al. FOREIGN PATENT DOCUMENTS Date Class Subclass Translation Document Country Number AL AM AN ΑO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR AS AT 3-19-04 **EXAMINER** DATE CONSIDERED *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		AB	5,281,927	01/94	Parker					
		AC	5,576,647	11/96	Sutardja et al.	-				
		AD	5,774,022	06/98	Griffin et al.					
		AE	5,516,434	05/95	Kootstra et al.					
		AF	5,412,665	05/95	Gruodis et al.					
-		AG	5,340,968	08/94	Watanabe et al.					*
		AH	5,907,789	05/99	Komatsu					
·		AI	5,780,916	07/98	Berger et al.	-				
		AJ	3,924,320	12/75	Altman et al.	*** ·				
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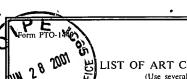
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	1	AB	4,384,288	05/17/83	Walton		340	825.34			
		AC	4,514,731	04/30/85	Falck et al.	EIVED	340	825.08			
		AD	4,868,908	09/19/89	Pless et al. JUL	0 8 2002	323	267			
		AE	5,103,156	04/07/92	Jones et al. Technolog	y Center 2600	320	35	_		
		AF	5,406,297	04/11/95	Caswell et al.		343	741			
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U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. Form PTO-1449 PATENT AND TRADEMARK OFFICE M140-325 09/822,063 LIST OF ART CITED BY APPLICANT APPLICANT Micron Technology, Inc. (Use several sheets if necessary) GROUP FILING DATE U.S. PATENT DOCUMENTS Document Date Name Class Subclass Filing Date If Appropriate Number 471 5,780,916 7/98 AA Berger et al. AB 5,143,820 09/92 Kotecha et al. 430 314 4,478,881 AC 10/84 Bartur et al. RECEIVED AD JAN 1 0 2002 ΑĖ Technology Center 2600 ΑF AG ΑH ΑI ΑJ FOREIGN PATENT DOCUMENTS Class Subclass Translation Document Date Country Number AL AM AN ΑO OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) Fink & Christiansen, ELECTRONIC ENGINEER'S HANDBOOK, 3rd Edition, McGraw-Hill, 1989, pgs. 8-22, 8-23 and 8-40. Wolf, SILICON PROCESSING FOR THE VLSI ERA VOLUME 2: PROCESS INTEGRATION, 1986, Lattice Press, pgs. 65 and 195. DATE CONSIDERED **EXAMINER** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. SERIAL NO. MI40-325 09/822,063 APPLICANT Micron Technology, Inc.

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary) FILING DATE GROUP March 30, 2001 2635 U.S. PATENT DOCUMENTS Filing Date If Appropriate Date Class Subclass *Examiner Name Document Initial Number AAΑB AC AD ΑĖ ΑF AG ΑH ΑI AJ ΑK FOREIGN PATENT DOCUMENTS Subclass Document Date Country Class Translation Number Yes No ΑL AM AN ΑO AP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) E.C. Young, "The Penguin Dictionary of Electronics", 1988, Market House Books, pages 7 and 124. AR Spel 3-19-00 EXAMINER DATE CONSIDERED

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			AD	6,122,494	9/19/00	Tuttle		455	193.1				
			AE	4,897,662	01/30/90	Lec et al.		343	701				
			AF	4,942,393	07/17/90	Waraksa et al.		340	825.72				
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